MANUEL FEDERICO DE LA CERRA

6885 Catamaran Drive; Carlsbad, CA 92011; (760) 809-5520

Email: manny@delacerralaw.com

EDUCATION:

University of California at San Diego

San Diego, CA

M.S. Electrical and Computer Engineering 2003

GPA: 3.3/4.0; Focus in semiconductor design and applied physics.

Stanford University

Stanford, CA

M.S. Civil and Environmental Engineering 1997 GPA: 3.6/4.0; Honors: Full Tuition Fellowship

USC Law School Los Angeles, CA

J.D. 1996

GPA: Top Third; Top 7% Third Year; Honors: Full Tuition Scholarship

Massachusetts Institute of Technology

Cambridge, MA

B.S. Civil and Environmental Engineering 1993

GPA: 3.7/4.0; 4th in class and graduated in three years at age 20; Honors: Dean's List

EXPERIENCE:

June 2005- Present The Law Office of Manuel de la Cerra

San Diego, CA

Sole Practitioner: Prosecuted patents in the electrical, mechanical and bio-medical arts. Conducted and managed all aspects of intellectual property litigation in a variety of forums including federal district court, the USPTO patent appeals board and the USPTO trademark appeals board. Prepared invalidity positions for patent reexamination

proceedings before the USPTO.

November 2004-June 2005

GEO2 Technologies Inc.

Woburn, MA

<u>General Counsel</u>: Advised Board of Directors and CEO on legal issues including employment, contract, and international and domestic intellectual property. Drafted codevelopment agreements, license agreements and employment agreements. Developed, managed and licensed an IP portfolio covering the company's core technology.

June 2003- November 2004

Paul, Hastings, Janofsky & Walker LLP

San Diego, CA

<u>Senior IP Associate</u>: Conducted and managed patent litigation matters, including summary judgment and injunction motions. Prepared experts reports in various fields. Drafted infringement opinions and conducted patent due diligence for corporate mergers and financing. Reviewed IP portfolios for infringement and non-infringement positions.

March 2001- June 2003

Kvocera Wireless Corp.

San Diego, CA

<u>Intellectual Property Litigation Counsel</u>: Conducted licensing and settlement conferences. Prepared valuation opinions and non-infringement/invalidity positions for use against adversaries. Prosecuted electronic and telecomm patents. Conducted and managed several patent litigation matters with multi-million dollar million budgets.

May 1998- October 2000

Lyon & Lyon, LLP

San Diego, CA

Patent Litigation and Prosecution Associate

July 1997-May 1998

Beveridge & Diamond, LLP

San Francisco, CA

BAR ADMISSIONS:

Cal. St. Bar No. 189,313; USPTO Reg. No. 45,776; Massachusetts (inactive)

PERSONAL INTERESTS:

Fluent in Spanish. Married with a young son and daughter. Enjoy restoring classic cars, including a 1930 Ford Model A, 1955 Ford T-bird, 1958 MGA and 1965 Ford Mustang.

REPRESENTATIVE LITIGATION RESPONSIBILITIES

Golden Hour Data Systems v. EMSCharts

Technology: Software patents for dispatching medical personnel

Responsibilities: Trial counsel responsible for trying inequitable conduct trial and over-turning a previous finding of

inequitable conduct. Court reversed previous ruling, finding that the patent was enforceable. The

Court is now preparing to enter multi-million dollar judgment for the client.

Sclimenti v. Stanford University

Technology: Gene therapy using integrases/recombinases

Responsibilities: Conduct patent/copyright litigation, including claim construction and review of thousands of pages

of technical documents to determine proper inventorship for a successfully licensed family of patents. Obtained a favorable ruling that client was indeed an inventor on the patents.

Wave Loch v. American Wave Machines

Technology: Aquatic wave making apparatus

Responsibilities: Successfully persuaded the USPTO to re-examine three US Patents asserted in district court based

on previously cited prior art. Managed the underlying patent infringement case, including preparing claim construction and preparing a successful motion to stay the pending litigation based on the USPTO re-examination orders. The stay essentially stopped the litigation after only nine months.

The USPTO issued a final rejection finding all originally asserted claims were invalid.

Greenlee v. Heritage Technologies

Technology: Electronic multi-meters

Responsibilities: Conduct patent litigation defense, including drafting an opinion of non-infringement. Prepared

sanctions motion based on the non-infringement position and successfully persuaded the patentee to

dismiss the case with prejudice within five months of filing the action.

REPRESENTATIVE IP PROSECUTION RESPONSIBILITIES

L1 Technologies

Responsibilities: Successfully persuaded the USPTO to re-examine a patent regarding GPS technology asserted in

district court based on previously cited prior art, as well as newly discovered prior art.

Touchdown Technologies

Responsibilities: Prepare and file patent applications covering novel probe structures. Review of prior art, take

disclosure from inventors, and draft application regarding novel probe structures and card for use in

semiconductor fabrication. Prepare response to office actions and coordinate and manage

international patent prosecution. Respond to and overcome USPTO rejections of TM applications.

Golden Hour Data Systems

Responsibilities: Review of prior art, take disclosure from inventors, and draft applications regarding novel medical

transportations methods and systems. Prepare response to office actions and coordinate and

manage international patent prosecution.

Successfully defended against re-examination requests seeking to invalidate the patent portfolio.

The patent portfolio was completely confirmed by the US Patent and Trademark Office.

RELEVANT GRADUATE LEVEL TECHNICAL COURSEWORK

Aquatic Chemistry

Assembly Computer Language

Biology I, II and III (Lab)

Phase Transformations

Probability and Statistics

Programming in C and Fortran

Bio-remediation Solid State Diffusion and Reaction Kinetics

Circuits and Systems I Solid State Electronics I
Computer Vision/Pattern Recognition Solid State Electronic Devices
Computerized Circuit Analysis Thermodynamics and Kinetics

Material Science VLSI Circuit Design